National Semiconductor

LM117HV/LM317HV 3-Terminal Adjustable Regulator

General Description

The LM117HV/LM317HV are adjustable 3-terminal positive voltage regulators capable of supplying in excess of 1.5A over a 1.2V to 57V output range. They are exceptionally easy to use and require only two external resistors to set the output voltage. Further, both line and load regulation are better than standard fixed regulators. Also, the LM117HV is packaged in standard transistor packages which are easily mounted and handled.

In addition to higher performance than fixed regulators, the LM117HV series offers full overload protection available only in IC's. Included on the chip are current limit, thermal overload protection and safe area protection. All overload protection circuitry remains fully functional even if the adjustment terminal is disconnected.

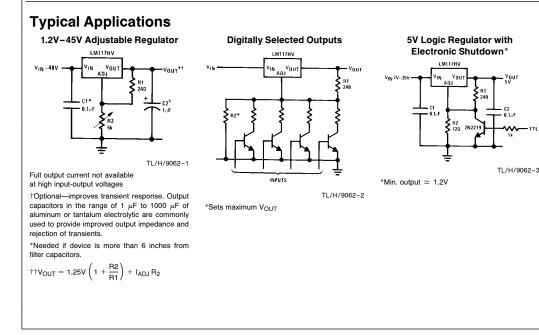
Normally, no capacitors are needed unless the device is situated more than 6 inches from the input filter capacitors in which case an input bypass is needed. An optional output capacitor can be added to improve transient response. The adjustment terminal can be bypassed to achieve very high ripple rejections ratios which are difficult to achieve with standard 3-terminal regulators.

Besides replacing fixed regulators, the LM117HV is useful in a wide variety of other applications. Since the regulator is "floating" and sees only the input-to-output differential voltage, supplies of several hundred volts can be regulated as long as the maximum input to output differential is not exceeded, i.e. do not short the output to ground. Also, it makes an especially simple adjustable switching regulator, a programmable output regulator, or by connecting a fixed resistor between the adjustment and output, the LM117HV can be used as a precision current regulator. Supplies with electronic shutdown can be achieved by clamping the adjustment terminal to ground which programs the output to 1.2V where most loads draw little current.

The LM117HVK STEEL and LM317HVK STEEL are packaged in standard TO-3 transistor packages, while the LM117HVH and LM317HVH are packaged in a solid Kovar base TO-39 transistor package. The LM317HVT uses a TO-220 plastic package. The LM117HV is rated for operation from -55° C to $+150^{\circ}$ C, and the LM317HV from 0° C to $+125^{\circ}$ C.

Features

- Adjustable output down to 1.2V
- Guaranteed 1.5A output current
- Line regulation typically 0.01%/V
- Load regulation typically 0.1%
- Current limit constant with temperature
- 100% electrical burn-in
- Eliminates the need to stock many voltages
- Standard 3-lead transistor package
- 80 dB ripple rejection
- Output is short-circuit protected
- P⁺ Product Enhancement tested



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Absolute Maximum Rat	ings				
If Military/Aerospace specified dev	vices are required,	Operating Junction Temperature Range)		
please contact the National Ser	miconductor Sales	LM117HV	-55°C to +150°C		
Office/Distributors for availability and specifications.		LM317HV	0°C to +125°C		
(Note 3)		Storage Temperature	-65°C to +150°C		
Power Dissipation	Internally limited	Lead Temperature (Soldering, 10 sec.)	300°C		
Input—Output Voltage Differential	+60V, -0.3V	ESD Tolerance (Note 4)	2000V		

Electrical Characteristics (Note 1)

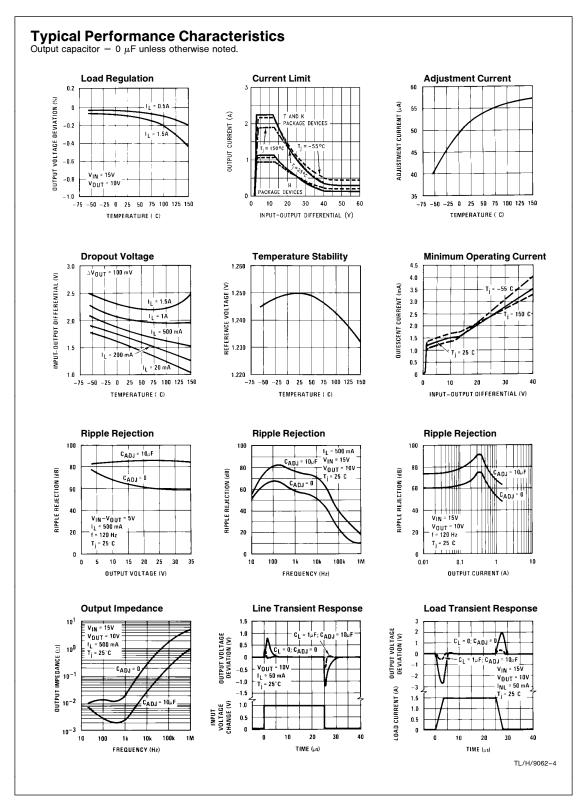
Parameter	Conditions	LM117HV			LM317HV			Units
		Min	Тур	Max	Min	Тур	Max	Units
Line Regulation	$\label{eq:total_states} \begin{array}{l} T_J=25^\circ C, \mbox{3V} \leq V_{IN}-V_{OUT} \leq 60V \\ \mbox{(Note 2)} \ I_L=10 \ \mbox{mA} \end{array}$		0.01	0.02		0.01	0.04	%/V
Load Regulation	$T_{J} = 25^{\circ}C, 10 \text{ mA} \leq I_{OUT} \leq I_{MAX}$		0.1	0.3		0.1	0.5	%
Thermal Regulation	$T_{J} = 25^{\circ}C$, 20 ms Pulse		0.03	0.07		0.04	0.07	%/W
Adjustment Pin Current			50	100		50	100	μA
Adjustment Pin Current Change	$ \begin{array}{l} 10 \text{ mA} \leq I_L \leq I_{MAX} \\ 3.0 \text{ V} \leq (V_{IN} - V_{OUT}) \leq 60 \text{V} \end{array} \end{array} $		0.2	5		0.2	5	μΑ
Reference Voltage	$\begin{array}{l} 3.0 \ V \leq (V_{IN} - V_{OUT}) \leq 60 V \text{, (Note 3)} \\ 10 \ \text{mA} \leq I_{OUT} \leq I_{MAX}, \ P \leq P_{MAX} \end{array}$	1.20	1.25	1.30	1.20	1.25	1.30	v
Line Regulation	$3.0V \leq (V_{IN} - V_{OUT}) \leq$ 60V, I_L = 10 mA, (Note 2)		0.02	0.05		0.02	0.07	%/V
Load Regulation	10 mA \leq I _{OUT} \leq I _{MAX} (Note 2)		0.3	1		0.3	1.5	%
Temperature Stability	$T_{MIN} \leq T_J \leq T_{MAX}$		1			1		%
Minimum Load Current	$(V_{IN} - V_{OUT}) = 60V$		3.5	7		3.5	12	mA
Current Limit	$\begin{array}{l} (V_{\text{IN}}-V_{\text{OUT}}) \leq 15V\\ \text{K, T Packages}\\ \text{H Package}\\ (V_{\text{IN}}-V_{\text{OUT}}) \leq 60V\\ \text{K, T Packages}\\ \text{H Package} \end{array}$	1.5 0.5	2.2 0.8 0.3 0.03	3.5 1.8	1.5 0.5	2.2 0.8 0.3 0.03	3.7 1.9	A A A
RMS Output Noise, % of VOUT	$T_J = 25^{\circ}$ C, 10 Hz \leq f \leq 10 kHz		0.003			0.003		%
Ripple Rejection Ratio	$\label{eq:VOUT} \begin{array}{l} V_{OUT} = 10V, f = 120 \mbox{ Hz} \\ C_{ADJ} = 10 \mu F \end{array}$	66	65 80		66	65 80		dB dB
Long-Term Stability	$T_{J} = 125^{\circ}C$		0.3	1		0.3	1	%
Thermal Resistance, Junction to Case	H Package T Package K Package		12 2.3	15 3		12 4 2.3	15 5 3	°C/W °C/W °C/W
Thermal Resistance, Junction to Ambient (no heat sink)	H Package T Package K Package		140 35			140 50 35		°C/W °C/W °C/W

Note 1: Unless otherwise specified, these specifications apply: $-55^{\circ}C \le T_J \le +150^{\circ}C$ for the LM117HV, and $0^{\circ}C \le T_J \le +125^{\circ}C$ for the LM317HV; $V_{IN} - V_{OUT} = 5^{\circ}$ and $I_{OUT} = 0.1A$ for the TO-39 package and $I_{OUT} = 0.5A$ for the TO-3 and TO-220 packages. Although power dissipation is internally limited, these specifications are applicable for power dissipations of 2W for the TO-39 and 20W for the TO-39 and TO-220. I_{MAX} is 1.5A for the TO-30 and TO-220 and 0.5A for the TO-39 package.

Note 2: Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

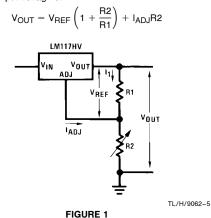
Note 3: Refer to RETS117HVH for LM117HVH or RETS117HVK for LM117HVK military specificatioins.

Note 4: Human body model, 1.5 k Ω in series with 100 pF.



Application Hints

In operation, the LM117HV develops a nominal 1.25V reference voltage, V_{REF} , between the output and adjustment terminal. The reference voltage is impressed across program resistor R1 and, since the voltage is constant, a constant current I₁ then flows through the output set resistor R2, giving an output voltage of



Since the 100 μ A current from the adjustment terminal rep-

resents an error term, the LM117HV was designed to minimize I_{ADJ} and make it very constant with line and load changes. To do this, all quiescent operating current is returned to the output establishing a minimum load current requirement. If there is insufficient load on the output, the output will rise.

External Capacitors

An input bypass capacitor is recommended. A 0.1 μ F disc or 1 μ F solid tantalum on the input is suitable input bypassing for almost all applications. The device is more sensitive to the absence of input bypassing when adjustment or output capacitors are used but the above values will eliminate the possibility of problems.

The adjustment terminal can be bypassed to ground on the LM117HV to improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. With a 10 μ F bypass capacitor 80 dB ripple rejection is obtainable at any output level. Increases over 10 μ F do not appreciably improve the ripple rejection at frequencies above 120 Hz. If the bypass capacitor is used, it is sometimes necessary to include protection diodes to prevent the capacitor from discharging through internal low current paths and damaging the device.

In general, the best type of capacitors to use are solid tantalum. Solid tantalum capacitors have low impedance even at high frequencies. Depending upon capacitor construction, it takes about 25 μF in aluminum electrolytic to equal 1 μF solid tantalum at high frequencies. Ceramic capacitors are also good at high frequencies; but some types have a large decrease in capacitance at frequencies around 0.5 MHz. For this reason, 0.01 μF disc may seem to work better than a 0.1 μF disc as a bypass.

Although the LM117HV is stable with no output capacitors, like any feedback circuit, certain values of external capaci-

tance can cause excessive ringing. This occurs with values between 500 pF and 5000 pF. A 1 μ F solid tantalum (or 25 μ F aluminum electrolytic) on the output swamps this effect and insures stability. Any increase of load capacitance larger than 10 μ F will merely improve the loop stability and output impedance.

Load Regulation

The LM117HV is capable of providing extremely good load regulation but a few precautions are needed to obtain maximum performance. The current set resistor connected between the adjustment terminal and the output terminal (usually 240 Ω) should be tied directly to the output of the regulator rather than near the load. This eliminates line drops from appearing effectively in series with the reference and degrading regulation. For example, a 15V regulator with 0.05Ω resistance between the regulator and load will have a load regulation due to line resistance of $0.05\Omega \times I_L$. If the set resistor is connected near the load the effective line resistance will be 0.05Ω (1 + R2/R1) or in this case, 11.5 times worse.

Figure 2 shows the effect of resistance between the regulator and 240Ω set resistor.

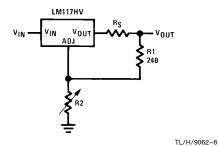


FIGURE 2. Regulator with Line Resistance in Output Lead

With the TO-3 package, it is easy to minimize the resistance from the case to the set resistor, by using two separate leads to the case. However, with the TO-5 package, care should be taken to minimize the wire length of the output lead. The ground of R2 can be returned near the ground of the load to provide remote ground sensing and improve load regulation.

Protection Diodes

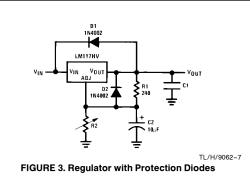
When external capacitors are used with *any* IC regulator it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low current points into the regulator. Most 10 μ F capacitors have low enough internal series resistance to deliver 20A spikes when shorted. Although the surge is short, there is enough energy to damage parts of the IC.

When an output capacitor is connected to a regulator and the input is shorted, the output capacitor will discharge into the output of the regulator. The discharge current depends on the value of the capacitor, the output voltage of the regulator, and the rate of decrease of V_{IN}. In the LM117HV, this discharge path is through a large junction that is able to sustain 15A surge with no problem. This is not true of other types of positive regulators. For output capacitors of 25 μF or less, there is no need to use diodes.

The bypass capacitor on the adjustment terminal can discharge through a low current junction. Discharge occurs when *either* the input or output is shorted. Internal to the LM117HV is a 50 Ω resistor which limits the peak discharge current. No protection is needed for output voltages of 25V or less and 10 μ F capacitance. *Figure 3* shows an LM117HV with protection diodes included for use with outputs greater than 25V and high values of output capacitance.

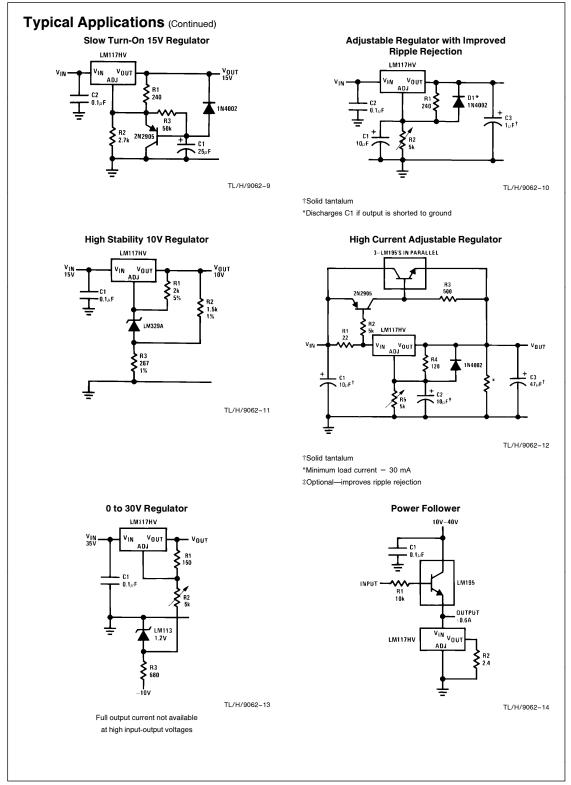
Current Limit

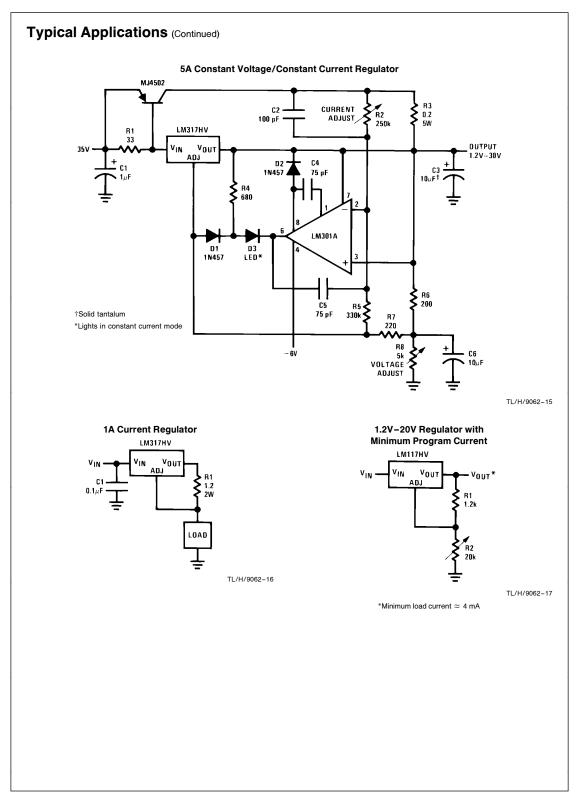
Internal current limit will be activated whenever the output current exceeds the limit indicated in the Typical Performance Characteristics. However, if during a short circuit condition the regulator's differential voltage exceeds the Absolute Maximum Rating of 60V (e.g. $V_{IN} \geq 60V, \, V_{OUT} = 0V$), internal junctions in the regulator may break down and the device may be damaged or fail. Failure modes range from an apparent open or short from input to output of the regulator, to a destroyed package (most common with the TO-220 package). To protect the regulator, the user is advised to be aware of voltages that may be applied to the regulator during fault conditions, and to avoid violating the Absolute Maximum Ratings.

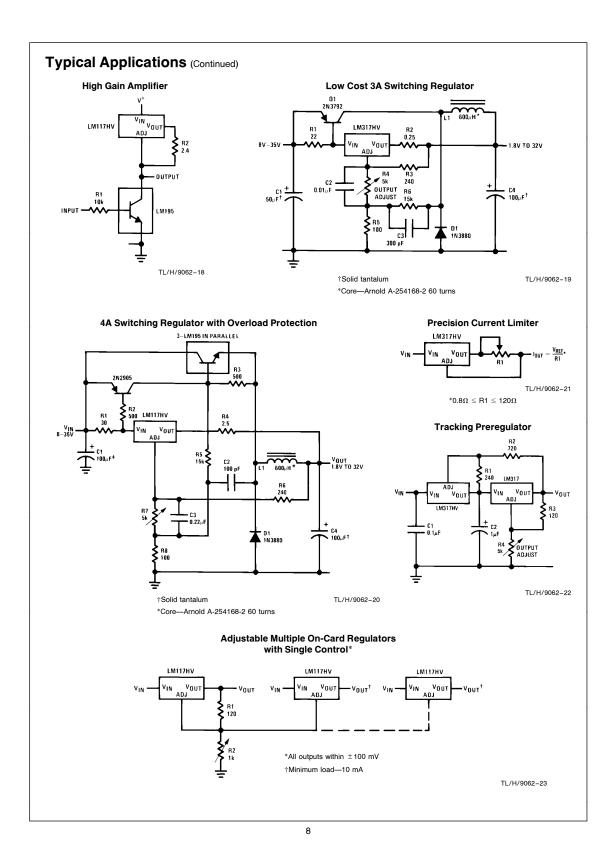


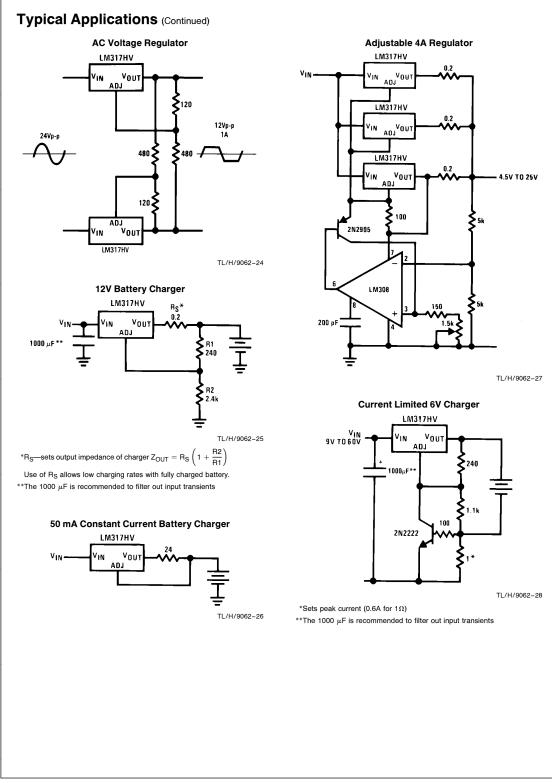
 $V_{OUT} = 1.25V \left(1 + \frac{R2}{R1}\right) + I_{ADJ}R2$ D1 protects against C1 D2 protects against C2

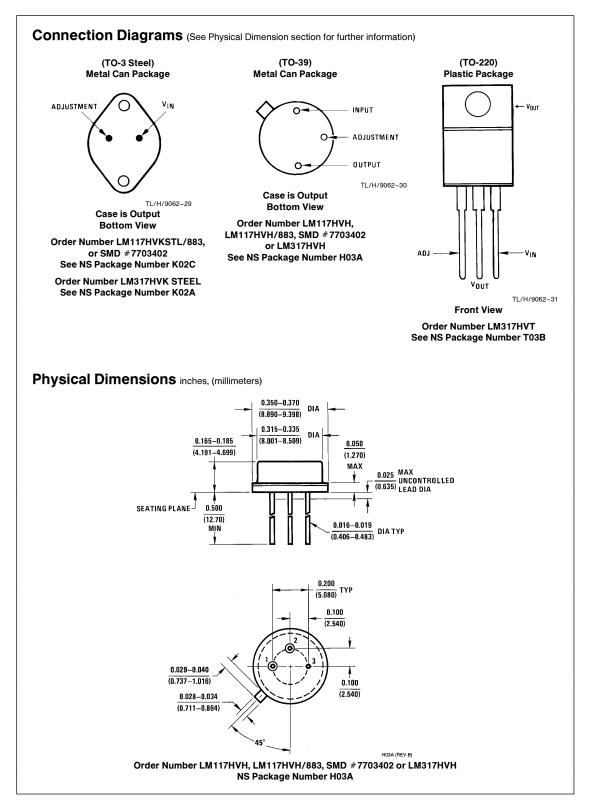
Schematic Diagram R2 310 R1 310 R3 R5 5.6k R19 370 R18 116 D2 6.3V R22 160 01 D1 6.3 012 011 С1 30 г 05 R9 180 **₹**^{R12} 72 ₹ 5.1k R14 R2(0.1 Vout **λλ** ADJ TL/H/9062-8

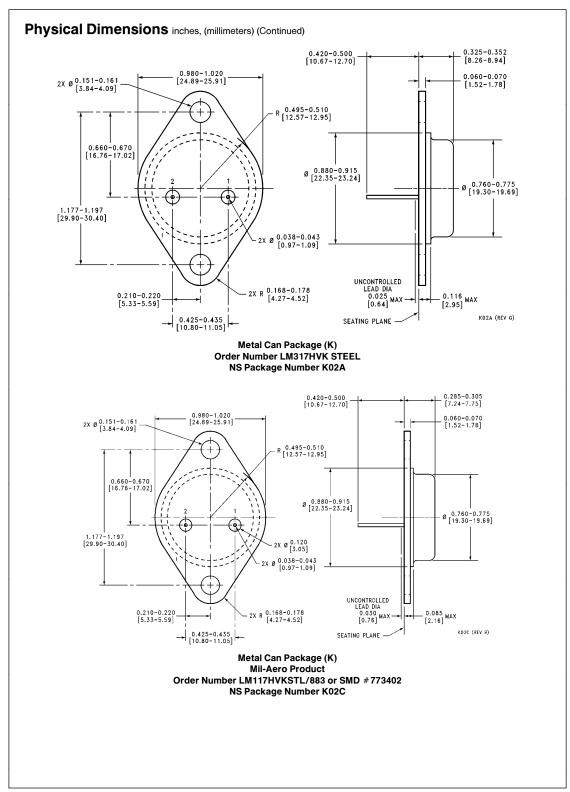


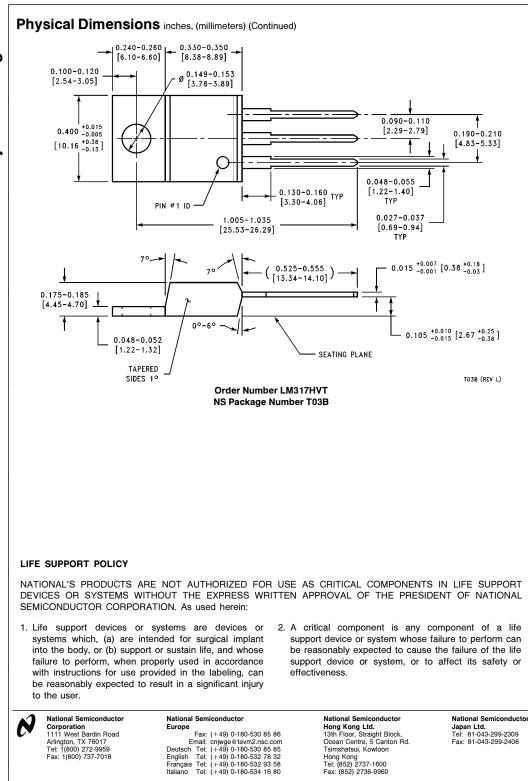












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